

(12) PATENT APPLICATION PUBLICATION

(21) Application No.202433002316 A

(19) INDIA

(22) Date of filing of Application :11/01/2024

(43) Publication Date : 18/07/2025

(54) Title of the invention : SECURITY ENHANCEMENT FOR QUANTUM INSPIRED MODEL IN QUANTUM DOT CELLULAR AUTOMATA

(51) International classification	:G06N0010000000, H04L0009080000, H04L0009320000, H04W0008180000, G09B0007020000	(71) Name of Applicant : 1)SUPARBA TAPNA Address of Applicant :22/2 Vidyasagar Sarani, Purba Barisha West Bengal India (72) Name of Inventor : 1)SUPARBA TAPNA
(31) Priority Document No	:NA	
(32) Priority Date	:NA	
(33) Name of priority country	:NA	
(86) International Application No	:NA	
Filing Date	:NA	
(87) International Publication No	: NA	
(61) Patent of Addition to Application Number	:NA	
Filing Date	:NA	
(62) Divisional to Application Number	:NA	
Filing Date	:NA	

(57) Abstract :

In late situation, CMOS innovation assumes a vital part in VLSI based IC innovation which is a combination of rationale models. The chips activities have shown an exceptional improvement in the many years. This addition of chips has depended on contracting the semiconductor. In CMOS innovation, the scaling system has prompted extreme difficulties of force utilization, actual aspects, and current spillages. Among the plausible arrangements, Quantum Cell Automata (QCA) is known as the top promising advances because of its possible applications in computational plans with engaging elements like low power utilization, high velocity activity and high gadget thickness. The QCA offers an improved answers for some circuit in particular adders, multipliers, recollections, cryptographic processors and nano specialized gadgets and so on. The consequences of actual equipment assaults are severer, and recuperation is troublesome which the paper is intrigued to zero in on equipment security. This paper examines the few security challenges like Figuring out, gate level net rundown overbuilding and a portion of the assaults and their countermeasures in view of circuits. A portion of the mystery key based cryptographic strategies challenges are secret key dispersion, age and all the more significantly getting these mystery keys from actual chases down is the significant issues. The Truly Unclonable Capabilities (PUFs) are newly utilized as a confident equipment security answer for ID and verification of circuits. Equipment security has arisen with the few issues like robbery, duplicating, and side channel assaults. This paper gives the different strategies that are introduced by numerous specialists to get the developed plan from the outsider in CMOS innovation. The paper is finished up with a requirement for research in security in QCA based circuit for equipment security in IC/IP.

No. of Pages : 32 No. of Claims : 10