



Certificate of Registration for a UK Design

Design number: 6472591

Grant date: 25 September 2025

Registration date: 16 September 2025

This is to certify that,

in pursuance of and subject to the provision of Registered Designs Act 1949, the design of which a representation or specimen is attached, had been registered as of the date of registration shown above in the name of

Mr. Sumit Roy, Mr. Ayan Mondal, Mr. Pulakesh Roy, Ms. Surabhi Solanki, Dr.

Shalini Kapoor, Dr. Dibyendu Roy

in respect of the application of such design to:

Computing Device for IoT Edge Node Operations

International Design Classification:

Version: 15-2025

Class: 14 RECORDING, TELECOMMUNICATION OR DATA PROCESSING EQUIPMENT

Subclass: 02 DATA PROCESSING EQUIPMENT AS WELL AS PERIPHERAL APPARATUS AND DEVICES

Adam Williams

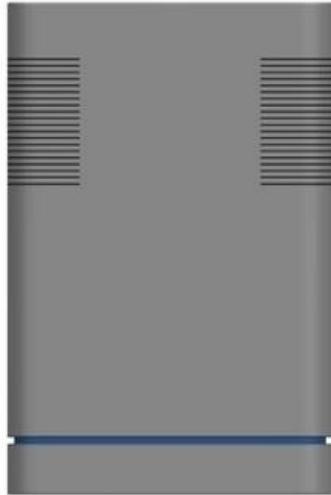
Comptroller-General of Patents, Designs and Trade Marks

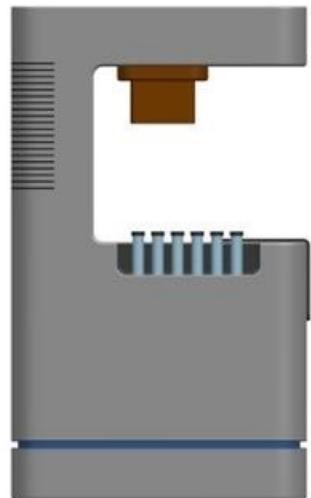
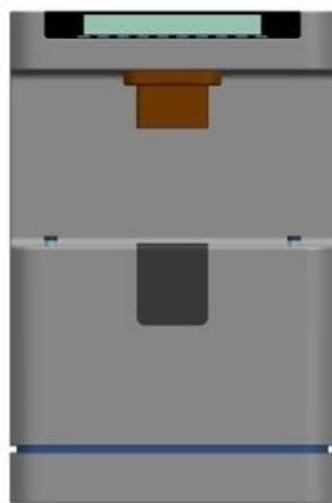
Intellectual Property Office

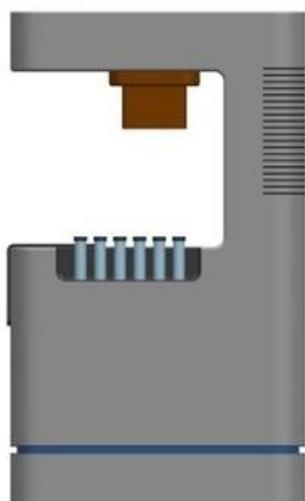
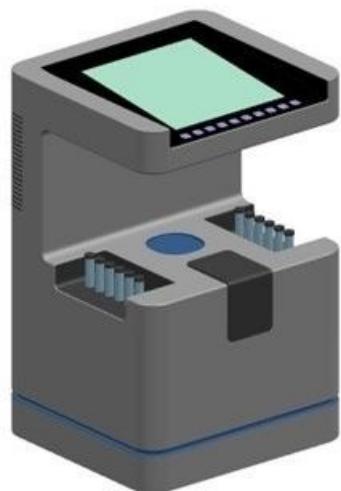
The attention of the Proprietor(s) is drawn to the important notes overleaf.

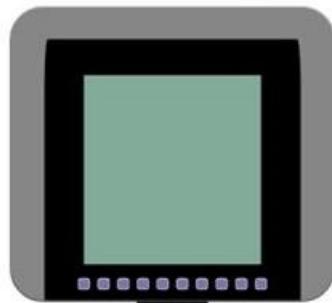


Representation of Designs









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